

MOBIVEIL TECHNOLOGIES INDIA PRIVATE LIMITED



HEAD OFFICE: EMM YES PARK, 3RD FLOOR, #69 (OLD # 137), JAWAHARLAL NEHRU ROAD, EKKATTUTHANGAL, CHENNAI – 600 032, TAMIL NADU, INDIA.

This is a multi-site certificate, additional site(s) are listed on the next page(s)

Bureau Veritas Certification Holding SAS – UK Branch certifies that the Management System of the above organization has been audited and found to be in accordance with the requirements of the Management System standard detailed below.

Standard

ISO 9001:2015

Scope of certification

- 1. SILICON INTELLECTUAL PROPERTIES (IPs) DEVELOPMENT
- 2. SYSTEM ON CHIP (SOC)/FIELD PROGRAMMABLE GATE ARRAY(FPGA) DESIGN, IMPLEMENTATION AND VERIFICATION
- 3. PRINTED CIRCUIT BOARD (PCB) DESIGN
- 4. EMBEDDED HARDWARE DESIGN AND DEVELOPMENT
- 5. SIGNAL INTEGRITY ANALYSIS
- 6. EMBEDDED SOFTWARE DEVELOPMENT, VALIDATION AND TESTING
- 7. APPLICATION SOFTWARE AND TEST AUTOMATION DEVELOPMENT

Original cycle start date: 09 December 2020

Expiry date of previous cycle: Not Applicable

Certification Audit date: 15 September 2020

Certification cycle start date: 09 December 2020

Subject to the continued satisfactory operation of the organization's Management System,

this certificate expires on: 08 December 2023

Certificate No. IND.20.4336/QM/U Version : 1 Revision date: 09 December 2020

Signed on behalf of BVCH SAS – UK Branch

Jagdheesh N. MANIAN Head – CERTIFICATION, South Asia

Commodities, Industry & Facilities Division

Certification body 5th Floor, 66 Prescot Street, London, E1 8HG, United Kingdom.

address:

Bureau Veritas (India) Private Limited (Certification Business) 72 Business Park, Marol Industrial Area, MIDC Cross Road "C",

Andheri (East), Mumbai – 400 093, India.





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MOBIVEIL TECHNOLOGIES INDIA PRIVATE LIMITED



Standard

ISO 9001:2015

SITE	ADDRESS	SCOPE
HEAD OFFICE CHENNAI	EMM YES PARK, 3RD FLOOR, #69 (OLD # 137), JAWAHARLAL NEHRU ROAD, EKKATTUTHANGAL, CHENNAI – 600 032, TAMIL NADU, INDIA.	 SILICON INTELLECTUAL PROPERTIES (IPs) DEVELOPMENT SYSTEM ON CHIP (SOC)/FIELD PROGRAMMABLE GATE ARRAY(FPGA) DESIGN, IMPLEMENTATION AND VERIFICATION PRINTED CIRCUIT BOARD (PCB) DESIGN EMBEDDED HARDWARE DESIGN AND DEVELOPMENT SIGNAL INTEGRITY ANALYSIS EMBEDDED SOFTWARE DEVELOPMENT, VALIDATION AND TESTING APPLICATION SOFTWARE AND TEST AUTOMATION DEVELOPMENT
SITE 1 - BENGALURU	#60 ADARSH REGENT, NEAR KORAMANGALA INNER RING ROAD, DOMLUR FLYOVER, DOMLUR, BENGALURU – 560 071, KARNATAKA, INDIA.	 SILICON INTELLECTUAL PROPERTIES (IPs) DEVELOPMENT SYSTEM ON CHIP (SOC)/FIELD PROGRAMMABLE GATE ARRAY(FPGA) DESIGN, IMPLEMENTATION AND VERIFICATION EMBEDDED HARDWARE DESIGN AND DEVELOPMENT EMBEDDED SOFTWARE DEVELOPMENT, VALIDATION AND TESTING

Revision date: 09 December 2020 Certificate No. IND.20.4336/QM/U Version: 1

Signed on behalf of BVCH SAS - UK Branch Jagdheesh N. MANIAN

Head - CERTIFICATION, South Asia Commodities, Industry & Facilities Division

Certification body

address:

Local office:

5th Floor, 66 Prescot Street, London, E1 8HG, United Kingdom.

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Further clarifications regarding the scope of this certificate and the applicability of the management system requirements may be obtained by consulting the organization. To check this certificate validity please call +91 22 6274 2000.