

# NVM - Express

Highly Configurable

Technology Independent

System Validated

## Overview

Mobiveil's Universal NVM Express Controller (UNEX) is highly flexible and configurable design targeted for both Enterprise and client class solutions that unlock the current and future potential of PCIe-based SSDs. The UNEX controller core efficiently supports multi-core architectures ensuring thread(s) may run on each core with their own queue and interrupt without any locks required. It provides support for end-to-end data protection, security and encryption as well as robust error reporting and management capabilities. The controller architecture is carefully tailored to optimize link and throughput utilization, latency, reliability, power consumption and silicon footprint.

Mobiveil's UNEX controller can be used along with its PCI Express® controller (GPEx), DDR4/3 (UMMC) and Enterprise Flash controller (EFC) IPs for a complete NVMe implementation.

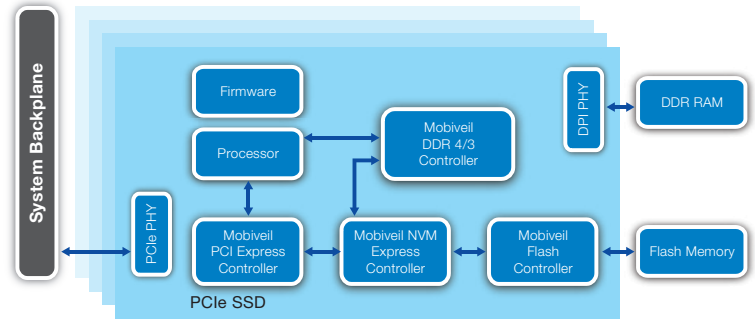
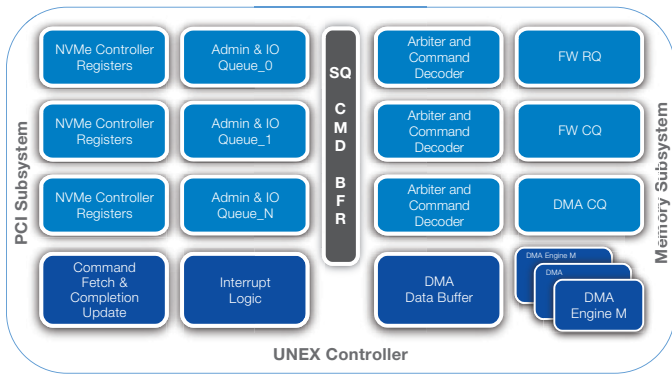
The UNEX controller comes with 3 flavors:

- Native UNEX Controller with proprietary control and Data path interfaces
- UNEX Controller with AXI Control and Data path interfaces for easy adoption in an SoC implementation
- UNEX Controller with Streaming Control and Data path interfaces (Avalon ST or AXI4 ST) for easy adoption in an SoC implementation with PCIe HIP or GPEx PCIe® controllers

UNEX Controller design is independent of implementation tools and target technology. Mobiveil solution allows the licensees to easily migrate among FPGA, Gate array and Standard cell technologies optimally.

## Features

- Compliant to NVM Express 1.4 specification
- Supports Multi-port Configuration for Multipath IO support
- Support for configurable number of IO Queues
- Support for configurable Queue depth
- Support for Round Robin or Weighted Round Robin with Urgent Priority arbitration mechanism
- Host memory page size support of 128MB
- Efficient and streamlined command handling
- Supports Fused Operations
- Supports All Optional Admin Commands
- Supports All Optional NVM Commands
- Supports Multi-Path IO and Namespace Sharing capabilities
- Supports Reservations
- Supports multiple name spaces
- Optional AXI interfaces for NVMe implementation in SoC
- Well defined Command Interface for local CPU to perform subsystem initialization and to handle all non-hardware accelerated commands



- Status : Gold
- Availability : Available now
- Language : Verilog
- Synthesis : DC, Genus, Quartus, Vivado
- Simulation : Cadence, Synopsys, Mentor
- Technology : 90nm ASIC or better, FPGA

## Specification

### Configurable Options

- Multi-port or Single Port
- Inclusion/Exclusion of AXI/Streaming interface modules
- Number of IO Queues
- IO Queue Depth
- Number of DMA Engines
- Number of Non Volatile Memory Channels
- Data Path Widths (64, 128, 256, 512)
- Data Buffer Size

### Design Attributes

- Highly modular and configurable design
- Layered architecture
- Fully synchronous design
- Supports both sync and async reset
- Software control for key features
- Multiple loop backs for debug

### Product Package

- RTL Code
- UVM based test bench and behavioral models
- Test cases
- Protocol checkers, bus watchers and performance monitors
- Configurable synthesis shell
- NVM Device FW Stack

### Documentation

- Design Datasheet
- Verification Guide
- Synthesis Guide
- NVM Device FW API User Guide

### Licensing Option

- NVMe Controller (UNEX)
- PCI Express® (GPEX) + NVMe (UNEX) - Integrated Solution
- PCI Express® (GPEX) + NVMe (UNEX) + UMMC + LDPC + EFC- Integrated Solution

## About Mobiveil

Mobiveil is a fast-growing technology company that specializes in development of Silicon Intellectual Property (SIP), platforms and solutions for AI/ML, Flash Storage, Data Center, 5G Telecom, Automotive and Industrial IOT applications. The Mobiveil team leverages decades of experience to deliver high-quality, production-proven, high-speed serial interconnect SIP cores, and custom and standard form factor embedded platforms to leading companies worldwide. With a highly motivated engineering team, dedicated integration support, a flexible business model, strong industry presence through strategic alliances and key partnerships, Mobiveil solutions add value to users by matching their product goals on time and within budget. Mobiveil is headquartered in Silicon Valley with engineering development centers located in Milpitas, CA, Chennai, Bangalore and Hyderabad, in India, and sales offices and representatives located in the U.S., Europe, Israel, Japan, Taiwan and the People's Republic of China.

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Patents and Patents pending.

OCT 2020 | Version 1.2